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Complete if Known Application Number 09/833,438 11 April 2001 Filing Date First Named Inventor Seven, et al. Art Unit 2682 Examiner Name Nguyen, Lee 2238-080 Attorney Docket Number

Sheet 1 of 2				Attorney Docket Number 2238-080			
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xaminer nitials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
1	 	Number-Kind Code ^{2 (# known)} US- 5,621,355	04/15/1997	Williams, et al.	<u> </u>		
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	T ⁸			
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Ilite as many sheets as necessary) (Ilite as many sheets as necessary) Filing Date Ait Unit 2882 RECEIVED Ait Unit 2882 RECEIVED At Unit 2882 RECEIVED Nguyen, Lee 238-080 NON PATENT LITERATURE DOCUMENTS Filing Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, oily and/or country where published. AJJIKUTTIRA, et al "A Fully-Integrated CMOS RFIC for Bluetooth Applications" 2001 IEEE International Solid-State Circuits Conference, Pgs 198-199, 2001. FILIOL, et a; "A 22mW Blutooth RF Transceiver with Direct RF Modulation and On-Chip IF Filtering" 2001 IEEE International Solid-State Circuits Conference, Pgs. 202-203, 2001. JOHNS & MARTIN "Analog Intergrated Circuit Design" Chapter 15, Pgs. 574-575, 578-584 & 626-635, Published by John Wiley & Sons, Inc., 1996. Examiner Signature Examiner Cite State Circuits Conference Considered Circuits Considered Circuits Conference Considered Circuits Circuits Considered Circuits Circuits Con	••••					Application Number	09/833,438			
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